

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	11600	((schematic\$5 circuit\$5) and (page\$1 window\$1 layer\$1 (multi adj page\$1)) and (edit\$5 modify\$4 chang\$4 alter\$4 (cut\$5 and past\$4))).clm.	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/30 12:59
L2	3863	((schematic\$5 circuit\$5) and (page\$1 window\$1 layer\$1 (multi adj page\$1)) and (edit\$5 modify\$4 chang\$4 alter\$4 (cut\$5 and past\$4))).clm.	US-PGPU B	OR	OFF	2005/09/30 13:00
L3	24	((schematic\$5 circuit\$5) and (page\$1 (multi adj page\$1)) and (edit\$5 (cut\$5 and past\$4))).clm.	US-PGPU B	OR	OFF	2005/09/30 13:01
L4	3	((schematic\$5 circuit\$5) and (page\$1) and number\$1 and cell\$1 and net\$1).clm.	US-PGPU B	OR	OFF	2005/09/30 13:03

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	23	viewdraw	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/07/28 12:55
S2	0	schematic near3 edit\$3	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/07/28 13:01
S3	470	schematic near3 edit\$3	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/07/28 13:23
S4	23	(schematic near3 edit\$3) and (cut same paste)	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/07/28 13:30
S5	246	(schematic near3 edit\$3) and ((semi adj conduct\$5) or IC or Chip or semiconductor)	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/07/28 13:33
S6	2	(schematic near3 edit\$3) near5 ((semi adj conduct\$5) or IC or Chip or semiconductor)	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/07/28 14:51
S7	4471	382/141,144,145,147,151,284, 294;29/833;438/16.ccis.	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/07/28 14:50
S8	1349	382/141,144,145,147,151,284, 294;29/833;438/16.ccis. and schematic	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/07/28 14:51
S9	3	382/141,144,145,147,151,284, 294;29/833;438/16.ccis. and (schematic near3 edit\$4)	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/07/28 14:51

S10	793	(382/141,144,145,147,151, 284,294;29/833;438/16.cccls. and schematic) and (((semi adj conduct\$5) or IC or Chip or semiconductor)	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/07/28 14:55
S11	145	(382/141,144,145,147,151, 284,294;29/833;438/16.cccls. and schematic) and (((semi adj conduct\$5) or IC or Chip or semiconductor) near4 (draw\$6 or plan\$1 or diagram\$1))	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/07/28 15:07
S12	48	((382/141,144,145,147,151, 284,294;29/833;438/16.cccls. and schematic) and (((semi adj conduct\$5) or IC or Chip or semiconductor) near4 (draw\$6 or plan\$1 or diagram\$1))) and (edit\$5 or modify\$5 or alter\$5)	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/07/28 14:56
S13	29662	(((semi adj conduct\$5) or IC or Chip or semiconductor) near4 (design))	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/07/29 10:48
S14	10	(((semi adj conduct\$5) or IC or Chip or semiconductor) near4 (design)) same (schematic near3 edit\$3)	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/07/29 12:14
S15	20	CAE same (((semi adj conduct\$5) or IC or Chip or semiconductor) near4 (design))	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/07/29 10:38
S16	476	((CAE or CAD) and (photomosaic\$1 or mosaic\$1))	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/07/29 10:48
S17	29697	(((semi adj conduct\$5) or IC or Chip or semiconductor) near4 (design))	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/07/29 10:48
S18	43	(((CAE or CAD) and (photomosaic\$1 or mosaic\$1))) and (((semi adj conduct\$5) or IC or Chip or semiconductor) near4 (design)))	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/07/29 10:48

S19	2	((semi adj conduct\$5) or IC or Chip or semiconductor) near4 (design) same (schematic near3 edit\$3) and (design near3 analysis)	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/07/29 12:16
S20	4	"5086477".pn.",5191213".pn.	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/07/29 12:54
S21	128	((reverse near3 engineer\$3) near5 (IC or (integrated near3 circuit\$)))	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/08/05 14:43
S22	57	((((reverse near3 engineer\$3) near5 (IC or (integrated near3 circuit\$)))) and schematic	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/08/05 14:44
S23	11	(((reverse near3 engineer\$3) near5 (IC or (integrated near3 circuit\$)))) and schematic) and edit\$3	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/08/05 14:50
S24	2	"4766516".pn.	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/08/05 14:51

S25	26	(US-6289116-\$ or US-6236746-\$ or US-5086477-\$ or US-5191213-\$ or US-5185812-\$ or US-6292766-\$ or US-5675499-\$ or US-6553543-\$ or US-5170263-\$ or US-5694481-\$ or US-5629860-\$ or US-5634113-\$ or US-6272668-\$ or US-6077308-\$ or US-6549222-\$ or US-5530372-\$ or US-6738957-\$ or US-5946478-\$ or US-5850348-\$ or US-5553001-\$ or US-4766516-\$ or US-6598210-\$ or US-6591155-\$ or US-6546539-\$).did. or (US-5740071-\$ or US-5901064-\$).did.	USPAT; DERWENT	OR	OFF	2004/08/06 10:47
S26	2	((US-6289116-\$ or US-6236746-\$ or US-5086477-\$ or US-5191213-\$ or US-5185812-\$ or US-6292766-\$ or US-5675499-\$ or US-6553543-\$ or US-5170263-\$ or US-5694481-\$ or US-5629860-\$ or US-5634113-\$ or US-6272668-\$ or US-6077308-\$ or US-6549222-\$ or US-5530372-\$ or US-6738957-\$ or US-5946478-\$ or US-5850348-\$ or US-5553001-\$ or US-4766516-\$ or US-6598210-\$ or US-6591155-\$ or US-6546539-\$).did. or (US-5740071-\$ or US-5901064-\$).did.) and (cutt\$3 and past\$5)	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/08/06 10:48

S27	159	(cut\$3 and past\$5) same schematic	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/08/06 10:50
S28	108	(cut\$3 same past\$5) same schematic	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/08/06 10:59
S29	52	(extract\$6 same past\$5) same schematic	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/08/06 10:59
S30	1393	(CAD or CAE) same ((cut same paste) or edit\$3)	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/08/06 14:44
S31	11	(CAD or CAE) same (cut same paste)	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/08/06 14:50
S32	20	(microsoft adj word) same (cut same paste)	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/08/06 14:53
S33	11457	OLE	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/08/07 11:01
S34	3034	Object near3 linking	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/08/07 11:02
S35	1113	Object near3 linking near3 embed\$4	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/08/07 11:02
S36	256	(Object near3 linking near3 embed\$4) and schematic	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/08/07 11:05

S37	86	(Object near3 linking near3 embed\$4) and schematic and circuit	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/08/07 11:07
S38	47	(Object near3 linking near3 embed\$4) and schematic and (circuit near3 design)	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/08/07 11:07
S39	1	(schematic near3 (layout\$ or design\$3)) and (edit\$4 near3 (cut\$3 and past\$4))	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/08/07 14:13
S40	12	(schematic near3 (layout\$ or design\$3)) and (edit\$4 same (cut\$3 and past\$4))	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/08/07 14:17
S41	25	(schematic near3 (layout\$ or design\$3 or edit\$)) and (edit\$4 same (cut\$3 and past\$4))	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/08/07 14:19
S42	13	((schematic near3 (layout\$ or design\$3 or edit\$)) and (edit\$4 same (cut\$3 and past\$4))) not ((schematic near3 (layout\$ or design\$3)) and (edit\$4 same (cut\$3 and past\$4)))	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/08/07 14:19
S43	9	"CAE" and (edit\$4 same (cut\$3 and past\$4))	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/08/07 14:32
S44	0	"6285369".pn. and (serach or find)	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/08/07 15:38
S45	0	"6285369".pn. and (search or find)	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/08/07 15:37
S46	2	"6285369".pn.	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/08/07 15:38

S47	1	"6285369".pn. and database	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/08/07 15:38
S48	4	"5694481".pn.",6285369".pn.	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/08/07 17:12
S49	4	"6199014".pn.", 20020029226".pn.	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/08/07 17:42
S50	6	"5974175".pn.",6115495".pn.", 5999161".pn.	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/08/07 17:44
S60	1364	edit\$4 same (schemat\$5 diagram\$4 plan\$4) same pages	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/05/11 16:23
S61	91	edit\$4 same (schemat\$5) same pages	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/05/11 16:24
S62	13	(edit edits editting) same (schemat\$5) same pages	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/05/11 16:30
S63	20	((edit edits editting) near4 (schemat\$5 diagram\$4 plan\$4 design\$4)) near5 pages	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/05/11 16:30
S64	519	(CAE or CAD) and ((edit edits editting editor) near4 (schematic\$1 diagram\$1 design\$1 circuit\$1))	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/05/12 12:58
S65	528	(CAE or CAD) and ((edit edits editting editor) near4 (schematic\$1 diagram\$1 design\$1 circuit\$1 architect\$6))	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/05/12 12:59

S66	188	S65 and (pages screens)	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/05/12 13:00
S67	26	viewdraw	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/05/15 15:56
S68	6	"6230305".pn.",5568397".pn.", 5544067".pn.	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/28 17:10
S69	3427	schematic\$4 same edit\$4	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/29 13:08
S70	379	(schematic\$4 same edit\$4) same page\$1	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/29 13:08
S71	74	S70 and cut\$4 and past\$4	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/29 12:57
S72	2421	((schematic\$4 circuit\$4) same edit\$4) same page\$1	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/29 12:56
S73	190	S72 and cut\$4 and past\$4	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/29 12:57
S74	32	S72 and (cut\$4 same past\$4)	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/29 12:57
S75	2006304	(schematic\$4 edit\$4)	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/29 13:08

S76	883	schematic\$4 near4 edit\$4	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/29 13:09
S77	168	S76 and netlist\$1	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/29 13:48
S78	2	"5694481".pn.	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/29 13:14
S79	883	schematic\$4 near4 edit\$4	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/29 13:48
S80	168	S79 and netlist\$1	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/29 13:48
S81	102	S80 and (cut\$4 extract\$4)	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/29 14:00
S82	10279	edit\$4 and (cutt\$4 and past\$4)	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/29 14:01
S83	3523	edit\$4 and (cutt\$4 and past\$4) and pages	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/29 14:01
S84	1137	S83 and schematic\$1	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/29 14:01
S85	49	edit\$4 and ((cutt\$4 same past\$4) same pages)	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/29 14:08

S86	2795	edit\$4 and ((schematic\$5 circuit) same pages)	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/29 14:11
S87	325	S86 and (cutt\$4 extract\$4) and past\$4	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/29 14:20
S89	1937	(edit\$4 modif\$6 alter\$4) same ((schematic\$5 circuit\$1) same pages)	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/29 14:16
S90	298	(edit\$4 modif\$6 alter\$4) same ((schematic\$5) same pages)	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/29 14:13
S91	30	S89 and netlist\$1	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/29 14:20
S92	6973	((edit\$4 modify\$4 alter\$4) same ((cutt\$4 extract\$4 remov\$4) and (plac\$4 add\$4 past\$4))) and (schematic\$1 and page\$1)	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/29 14:22
S93	4015	((edit\$4 modify\$4 alter\$4) same ((cutt\$4 extract\$4 remov\$4) same (plac\$4 add\$4 past\$4))) and (schematic\$1 and page\$1)	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/29 14:22
S94	13	netlist same ((view\$4 display\$4) same split\$4)	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/29 15:05
S95	13	netlist\$1 same ((view\$4 display\$4) same split\$4)	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/29 15:05
S96	8	("4790027" "4791586" "5086477" "5191213" "5222158" "5224178" "5463561" "5465163").PN.	US-PGPU B; USPAT; USOCR	OR	OFF	2005/09/29 15:19

S97	10	("4790027").URPN.	USPAT	OR	OFF	2005/09/29 15:23
S98	16	("5694481").URPN.	USPAT	OR	OFF	2005/09/29 15:23
S99	49	(CAD CAE) and schematic\$1 and ((view\$4 display\$4) near6 netlist\$1)	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/29 15:35
S100	7907	382/100, 113;700/182;706/919;716/9, 10,11,12,17;715/517,514,700, 724.ccis.	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/30 09:41
S101	554	S100 and ((schematic\$ circuit\$1) and page\$1 and edit\$4)	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/30 09:44
S102	821	S100 and netlist\$1	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/30 09:42
S103	62	S101 and netlist\$1	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/30 09:42
S104	138	S100 and ((schematic\$ circuit\$) same page\$1) and edit\$4	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/30 10:26
S105	30	S104 and netlist\$1	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/30 09:47
S106	2	S104 and (cut\$4 and past\$4)	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/30 09:48
S107	1979	((schematic\$ circuit\$) near5 (page\$1 or (multi adj page\$1) or multipage\$1 or multi-page\$1)) and edit\$4	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/30 12:45

S10 8	441	((schematic\$ circuit\$) near5 (page\$1 or (multi adj page\$1) or multipage\$1 or multi-page\$1)) same edit\$4	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/30 12:45
S10 9	1	S108 and ((cutt\$4 and past\$4) same page\$1)	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/30 10:29
S11 0	5	S108 and (cutt\$4 and past\$4)	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/30 10:29
S11 1	25	715/724.ccls.	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/30 10:39
S11 2	2147	(schematic\$1 circuit\$1) same (edit\$4) same (page\$1 or (multi adj page\$1) multipage\$1)	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/30 12:44
S11 3	1282	((schematic\$ circuit\$) near5 (window\$1)) and edit\$4	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/30 12:48
S11 4	21	S113 and ((cut\$5 same past\$4) same window\$1)	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/30 12:46
S11 5	30	S113 and netlist\$1	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/30 12:50
S11 6	5	(CAD CAE) same (window\$1 page\$1) same netlist\$1 same (schematic\$1 circuit\$1)	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/30 12:52
S11 7	5	(CAD CAE) same (window\$1 page\$1) same netlist\$1 and (schematic\$1 circuit\$1)	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/30 12:52

S11 8	48	(CAD CAE) same (window\$1 page\$1) and netlist\$1 and (schematic\$1 circuit\$1)	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/30 12:52
S11 9	43	S118 not S117	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/30 12:52
S12 0	11600	((schematic\$5 circuit\$5) and (page\$1 window\$1 layer\$1 (multi adj page\$1)) and (edit\$5 modify\$4 chang\$4 alter\$4 (cut\$5 and past\$4))).clm.	US-PGPU B; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/09/30 12:59
S12 1	3863	((schematic\$5 circuit\$5) and (page\$1 window\$1 layer\$1 (multi adj page\$1)) and (edit\$5 modify\$4 chang\$4 alter\$4 (cut\$5 and past\$4))).clm:	US-PGPU B	OR	OFF	2005/09/30 13:00
S12 2	24	((schematic\$5 circuit\$5) and (page\$1 (multi adj page\$1)) and (edit\$5 (cut\$5 and past\$4))).clm.	US-PGPU B	OR	OFF	2005/09/30 13:01
S12 3	3	((schematic\$5 circuit\$5) and (page\$1) and number\$1 and cell\$1 and net\$1).clm.	US-PGPU B	OR	OFF	2005/09/30 13:03

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Title

Technical publishing tools streamline documentation process.

Author(s)

Harding-B.

Source

Computer-Design (USA), vol.28, no.9, p.59-61, 1 May 1989.

CODEN

CMPDAM.

ISSN

ISSN: 0010-4566.

Publication year

1989.

Language

EN.

Publication type

J Journal Paper.

Treatment codes

G General or Review; P Practical.

Abstract

An electronic engineering manager may well face a documentation problem that rivals the design job itself. The massive problems associated with producing documentation in a timely manner prompted the emergence of a new class of tools called electronic technical publishing (ETP). ETP includes many desktop publishing capabilities but goes far beyond the process of generating printable **pages**. Mentor Graphic's Context Division offers a set of ETP tools-Context Writer, Context **Editor** and Context Documentor-that support a procedure called inclusion by reference. The writer of the manual can include **schematic** by simply telling the ETP system the **schematic's** location thus the documentation automatically includes any changes made by the engineer. In the Context system, security procedures can be invoked that let reviewers check a document in a read-only mode, but with the ability to add annotations or comments to the document without changing the document's text. To help manage the documentation effort, ETP tools help with structure maintenance, change control, and file security and access control. Access rights may change as a document progresses through its life cycle. (0 refs).

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Keywords

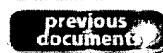
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